

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3511	tri adj state adj (inverter or buffer)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:16
L2	161	tri adj state adj (inverter or buffer) with clock adj signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:38
L3	2	("5376829"   "5625303").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/09/14 13:23
L4	7	("6087886").URPN.	USPAT	OR	OFF	2005/09/14 13:24
L5	3350	1 not 2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:38
L6	148	5 and (series or serial or cascad\$3 or cascod\$3) adj2 transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:52
L7	2255	326/82,83,85.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:51
L8	310	7 and (series or serial or cascad\$3 or cascod\$3) adj2 transistor\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:52
L9	296	8 not 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 13:52
L10	67	327/18,20,108.ccls. and @pd>="20050501"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/14 15:36